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IEEE Transactions on Neural Networks and Learning Systems Outstanding Paper Award Published in 2016: Tong Wang, Huijun Gao, and Jianbin Qiu, for the paper entitled "A Combined Adaptive Neural Network and Nonlinear Model Predictive Control for Multirate Networked Industrial Process Control," IEEE Transactions on Neural Networks and Learning Systems, vol. 27, no. 2, pp. 416-425, February 2016.

[Past Recipients - IEEE Computational Intelligence Society](#)

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3090 IEEE TRANSACTIONS ON COMMUNICATIONS, VOL. 59, NO. 11, NOVEMBER 2011 Reduced-Complexity Coherent Versus Non-Coherent QAM-Aided Space-Time Shift Keying Shinya Sugiura, Member, IEEE, Chao Xu, Student Member, IEEE, Soon Xin Ng, Senior Member, IEEE, and Lajos Hanzo, Fellow, IEEE Abstract-A novel reduced-complexity near-optimal detection

[3090 IEEE TRANSACTIONS ON COMMUNICATIONS, VOL. 59, NO. 11 ...](#)

2860 IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS-I: REGULAR PAPERS, VOL. 58, NO. 12, DECEMBER 2011 in a design independently (local ABB), to mitigate D2D and WID variations. However, supplying so many separate voltages inside a die results in a large area overhead. On the other hand, using the same body bias for all devices on the same die (global

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222 IEEE TRANSACTIONS ON NEURAL SYSTEMS AND REHABILITATION ENGINEERING, VOL. 19, NO. 3, JUNE 2011 feature and, hence, no within-group correlation is modeled. From this point of view, the proposed CLDA algorithm is a generalized version of DLDA. It aims to achieve improved decoding accuracy by accurately capturing the correlation

[IEEE TRANSACTIONS ON NEURAL SYSTEMS AND REHABILITATION ...](#)

2420 IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS-I: REGULAR PAPERS, VOL. 58, NO. 10, OCTOBER 2011 A SIMD Cellular Processor Array Vision Chip With Asynchronous Processing Capabilities Alexey Lopich, Member, IEEE, and Piotr Dudek, Senior Member, IEEE Abstract-This paper describes an architecture and implemen-

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TO APPEAR IN IEEE TRANSACTIONS ON WIRELESS COMMUNICATIONS 12 our meta algorithm. In the following, we use a batch of tasks (include Nb tasks) as an example to introduce the two processes in Fig. 2. The same neural network architecture is used in inner-task update and cross-task update.

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